

etc-nand

Overview

etc-nand is a real-world ETC.A CPU built from almost only quad NAND gate ICs (74hc00)

It will probably be finished in a few months.

Estimates

Estimated gate count:

- 2800 NAND gates
- 320 tristate buffers

Estimated component counts:

- 700x 74hc00 quad NAND gates
- 40x 74HC54 octal tristate buffers
- a few simple resistors

Planned Specifications

ETC.A base instruction set + byte operations + S&F + Von Neumann

The CPU will communicate with peripherals over a 16 bit data + 15 bit address memory bus

Purchase

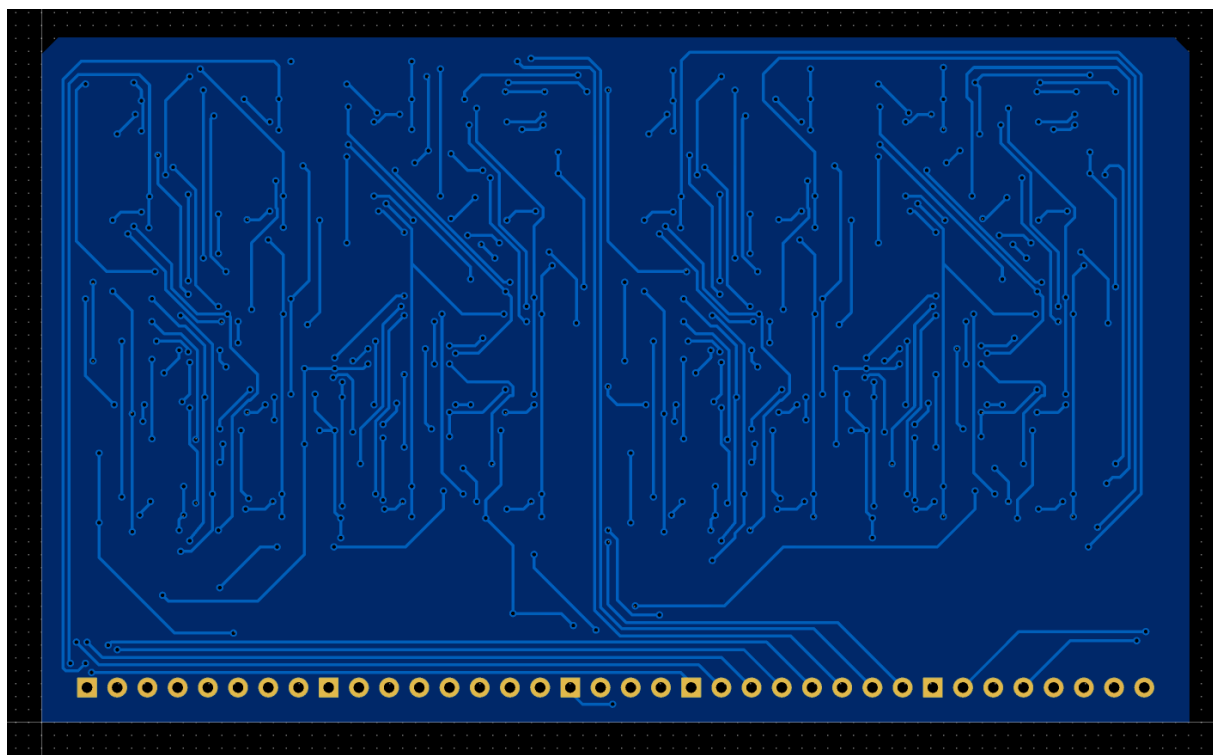
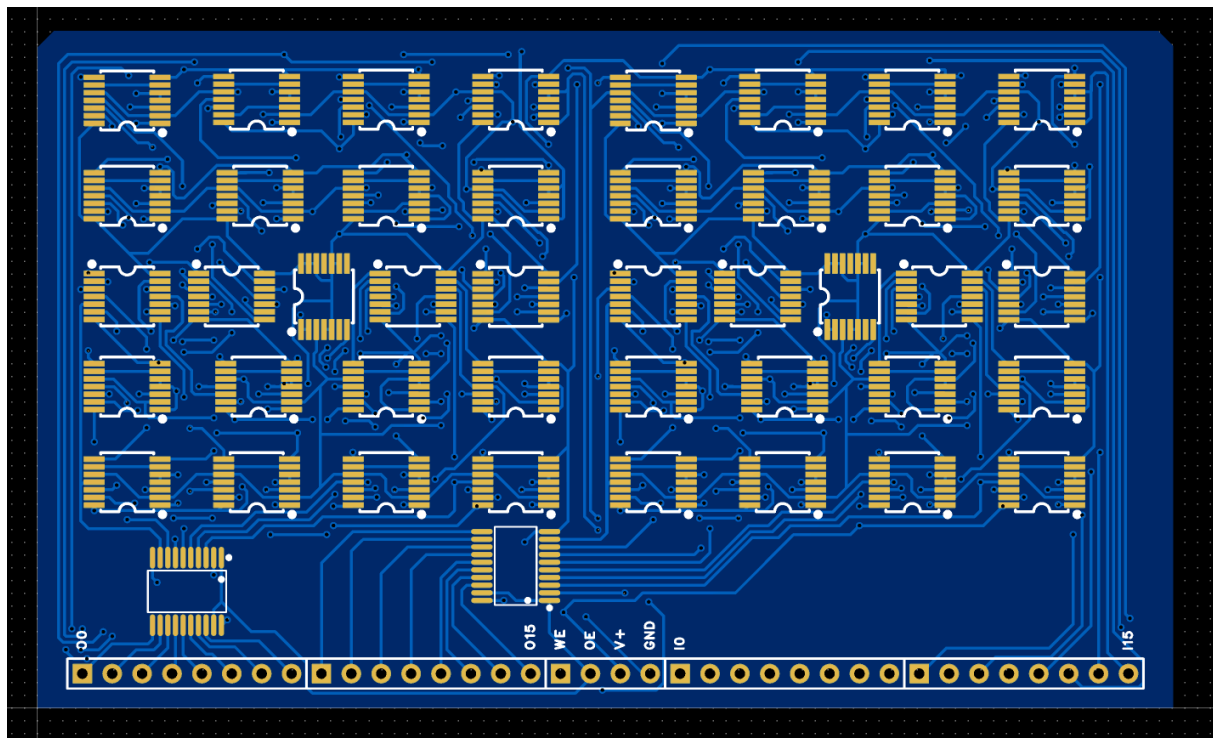
You will be able to purchase one in the future.

Stay tuned!

Images

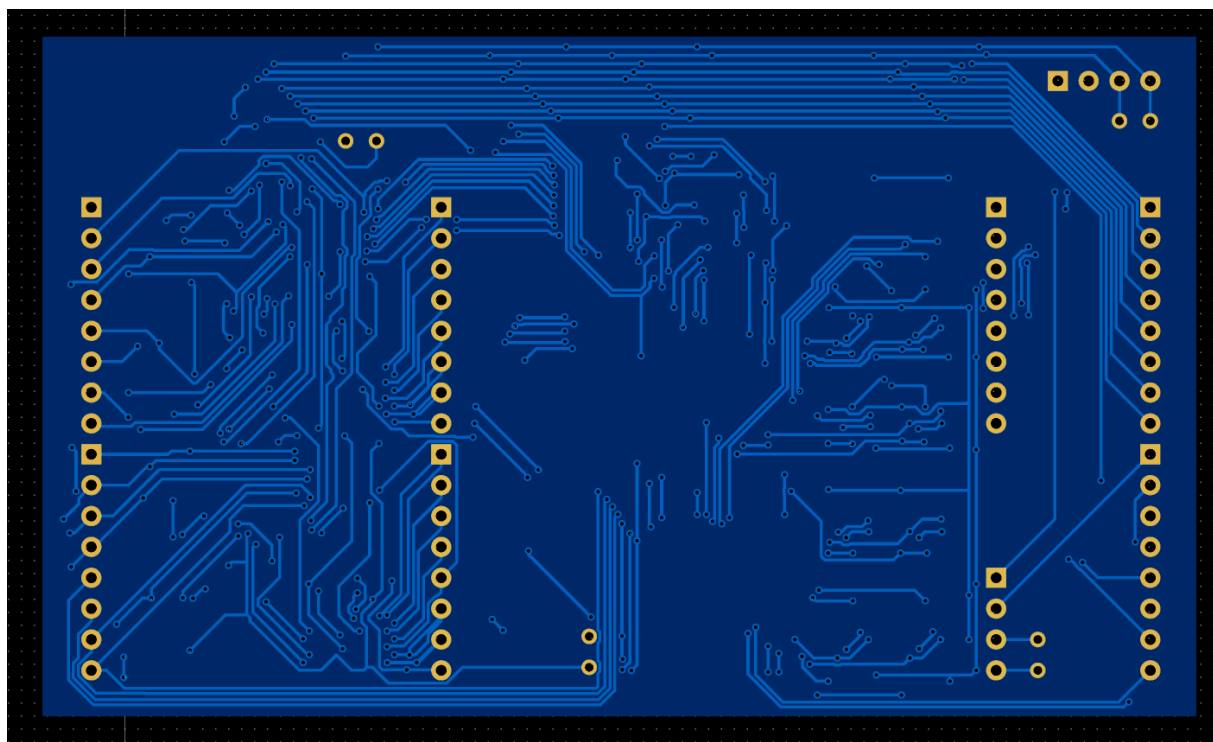
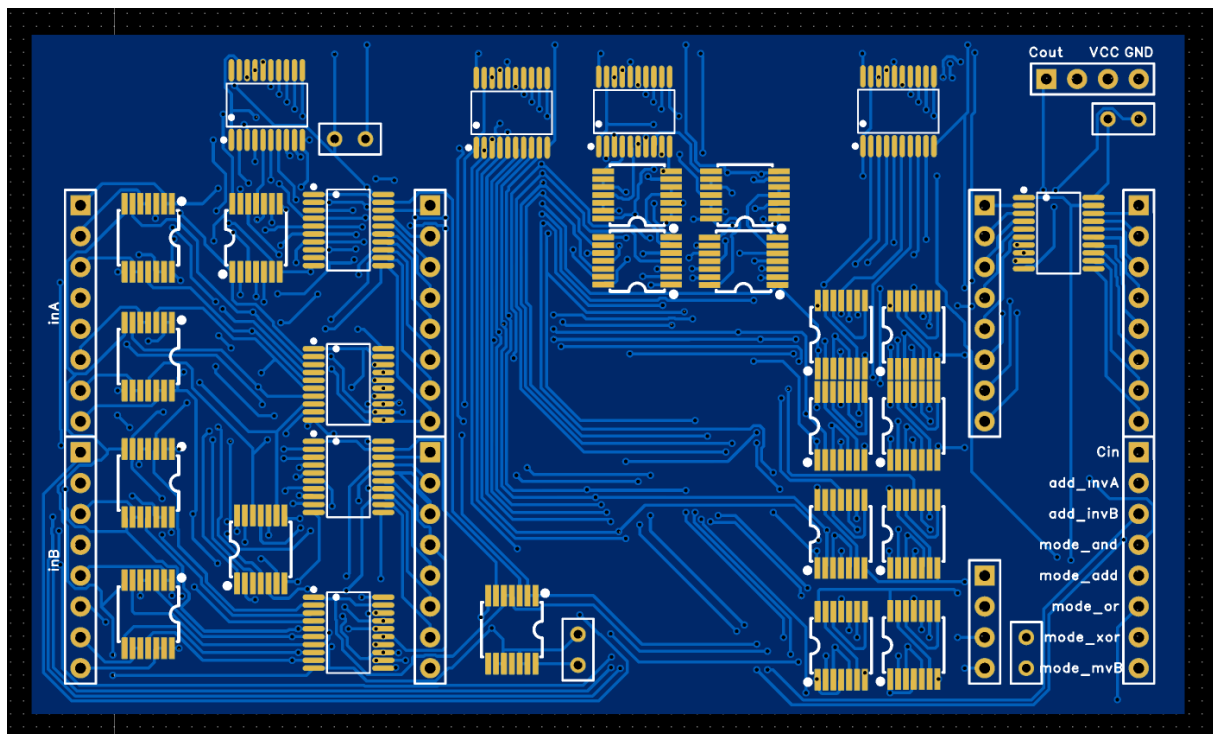
Images of PCBs that are either already manufactured or currently beeing manufactured by JLCPCB.

16 bit register



8 bit ALU slice

A 8 bit adder module will be placed in the middle



8 bit adder

